

**STATE BOARD OF TECHNICAL EDUCATION AND TRAINING
TELANGANA::HYDERABAD**

To

From

Sri UVSN Murthy, M.Tech,
Secretary,
State Board of Technical Education and
Training, S.V. Bhavan, Masab Tank,
HYDERABAD.500028

The Principals of all Polytechnics/
Institutions, (including Govt./ private
polytechnics in the Telangana State)
and all II Shift Polytechnics
in existing Engineering Colleges

Sir,

Sub:- SBTET– Exams –C-16s V Semester & C-16 VII Semester Unit Test – Schedule for theory marks entry in to portal – issued– Reg.

Ref: 1) Academic Calendar 2019-20.

2) This office Lr. No.SBTET-EE-1/EE-2/8/2019-EE-1 dated: 01-08-2019

With reference to the subject cited above, please find Schedule for Unit Test theory Marks Entry in to SBTET portal for C16S (5th Semester) & C16 (7th Semester) Unit Test Examinations.

S. No	Scheme	Entry of Unit Test Theory Marks in to portal	
		<i>Last date</i>	<i>Last date with Penalty</i>
1	C16S (5 th Semester) & C16 (7 th Semester)	11-09-2019	14-09-2019

All the Principals are requested to instruct the concerned Head of Sections to enter ss.sbtet.telangana.gov.in in Assessment Module and confirm and take print of the same and file for record. If confirmation button is not pressed before last date the marks entered in the portal will be taken and results will be processed. It is decided to implement penalty of **Rs.1000** for late entry of marks per semester per branch as per the above schedule.

If Unit Test theory marks are not entered in to portal as per the above stipulated schedule, the branch for which marks are not entered will be de affiliated and again the institute has to pay affiliation fee afresh for the current academic year. For any clarifications please call to helpdesk 040-23222192 during office working hours [sbtet- helpdesk@telangana.gov.in](mailto:sbtet-helpdesk@telangana.gov.in)

Yours faithfully,
Signature valid

Digitally signed by: MURTHY
UVSN
Date: 2019.08.29 03:41:34 IST
Reason: Approved

Secretary

Copy to System Analyst to enable portal accordingly.